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PATENTS

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Hirokazu MIYAZAKI

Confirmation No. 8771

Serial No. 09/788,596

Group 2811

Filed February 21, 2001

Examiner N. Parekh

MOUNTING STRUCTURE OF SEMICONDUCTOR
DEVICE AND MOUNTING METHOD THEREOF

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in the corresponding Japanese Application Serial No. 2000-049904, filed February 25, 2000. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. An English translation of the enclosed portion is also attached hereto.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a

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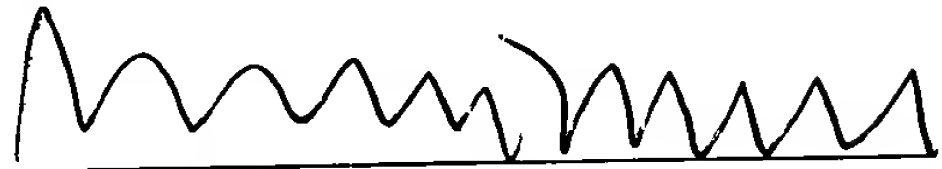
S.N. 09/788,596

counterpart foreign application not more than three months prior to the filing of this Statement.

Respectfully submitted,

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By



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March 3, 2003